

*J.F.W.*IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**INFORMATION DISCLOSURE STATEMENT**
37 C.F.R. § 1.97(b)

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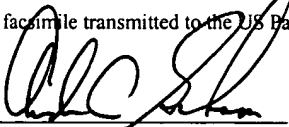
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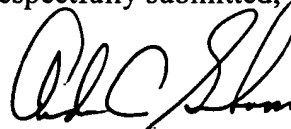
FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

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Respectfully submitted,



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